

S63	9	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware) same protection) and mips	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:14
S65	0	Mips near3 instruction near3 set	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S66	535	Mips near3 instruction near3 set	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S67	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S68	3	(Mips near3 instruction near3 set) near4 chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:17
S69	7	(Mips near3 instruction near3 set) near10 chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:36

S70	126	(chip or (micro near3 chip or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S71	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S73	995	(chip or (micro near3 chip or (smart near3 card))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S74	1	S73 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S75	525	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S76	1	S75 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39

S77	1165449	(chip or (micro near3 chip)) or (microprocessor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S78	75	S77 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S79	824	(hardware same chip same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S80	1	S79 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S81	4169	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S82	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S83	2	S82 and S81	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S84	3787	711/163	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57

S85	2	S82 and S84	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S86	1332	711/164	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S88	1829	711/145	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S89	3	S82 and S88	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57

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